

FIG. 1

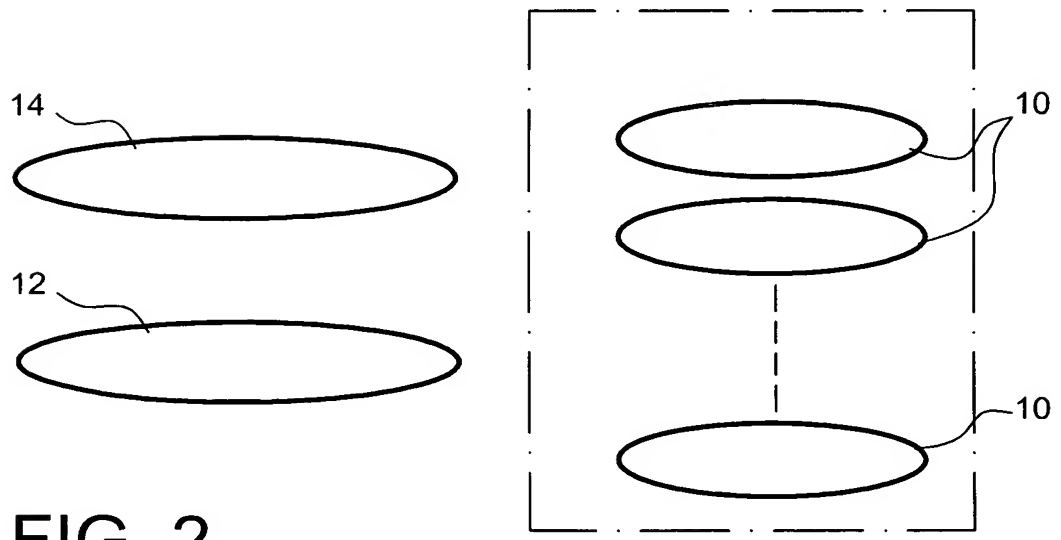


FIG. 2

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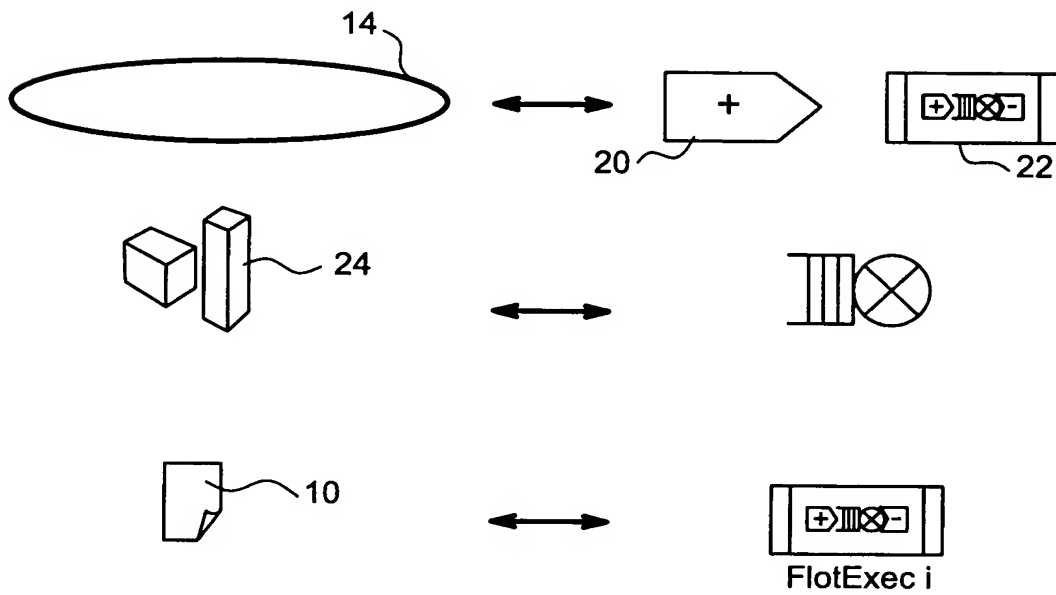


FIG. 3

exec(t)



FIG. 4a

publish (v1, ... vn)

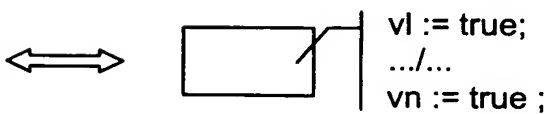


FIG. 4b

end

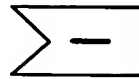
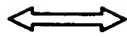


FIG. 4c

delay(t)

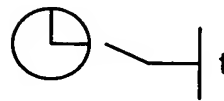


FIG. 4d

expect(c)

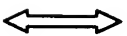


FIG. 4e

oriented arc

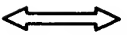


FIG. 4f

N oriented arcs

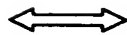


FIG. 4g

Emissions

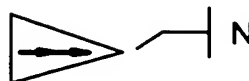
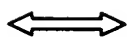


FIG. 4h

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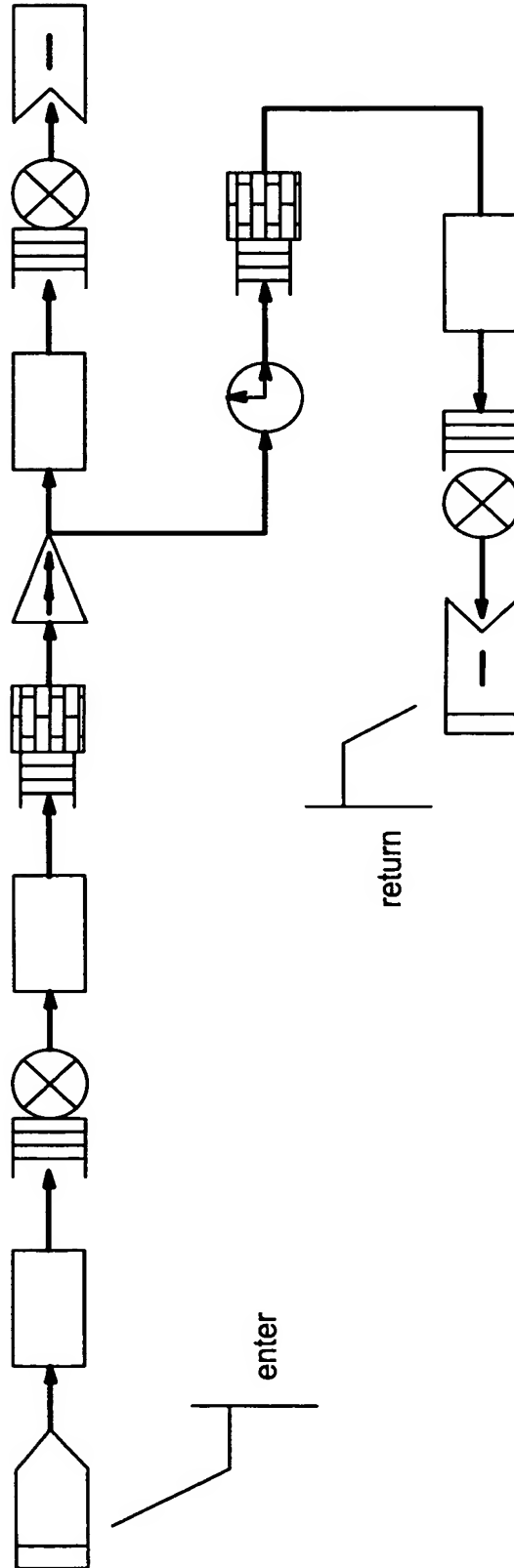


FIG. 5

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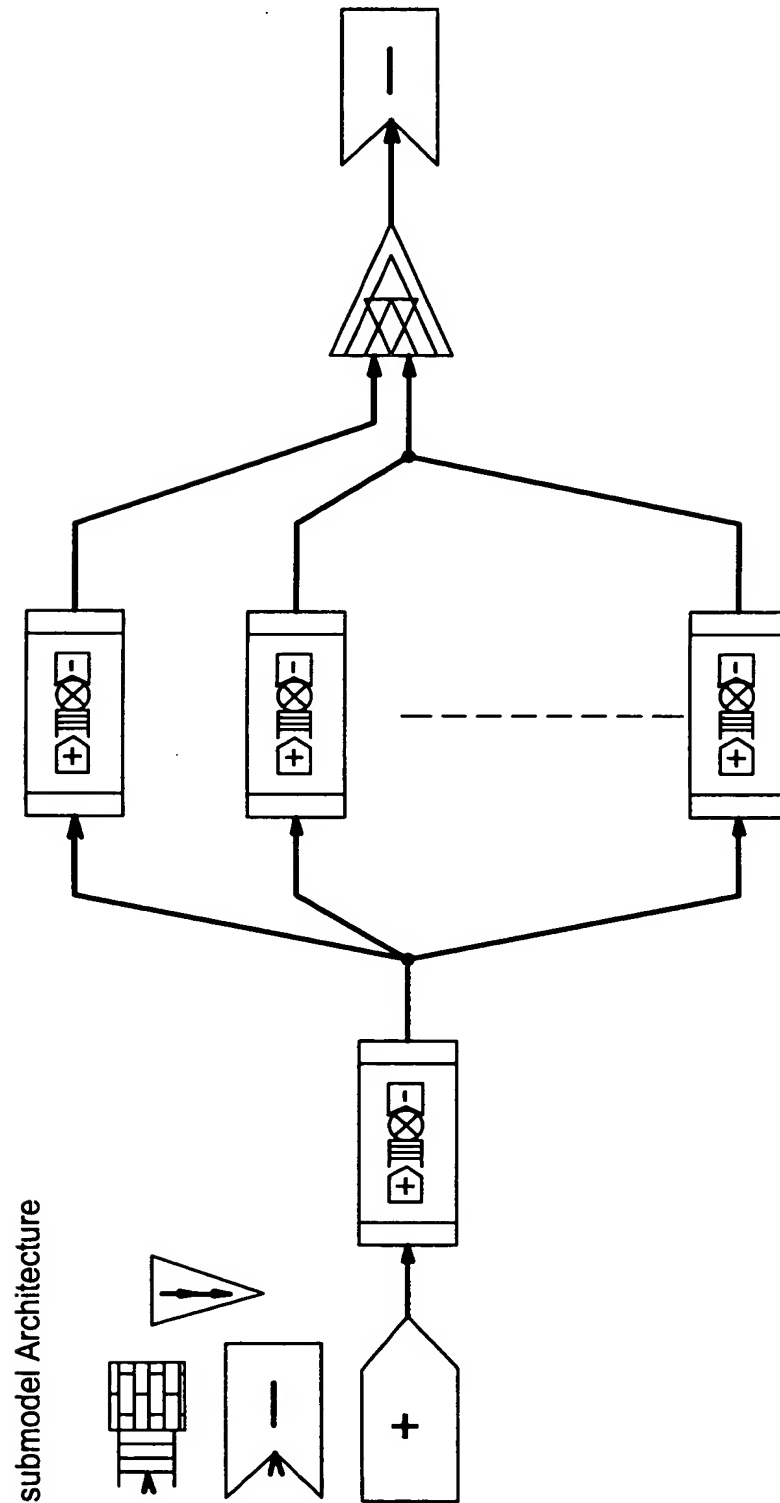


FIG. 6

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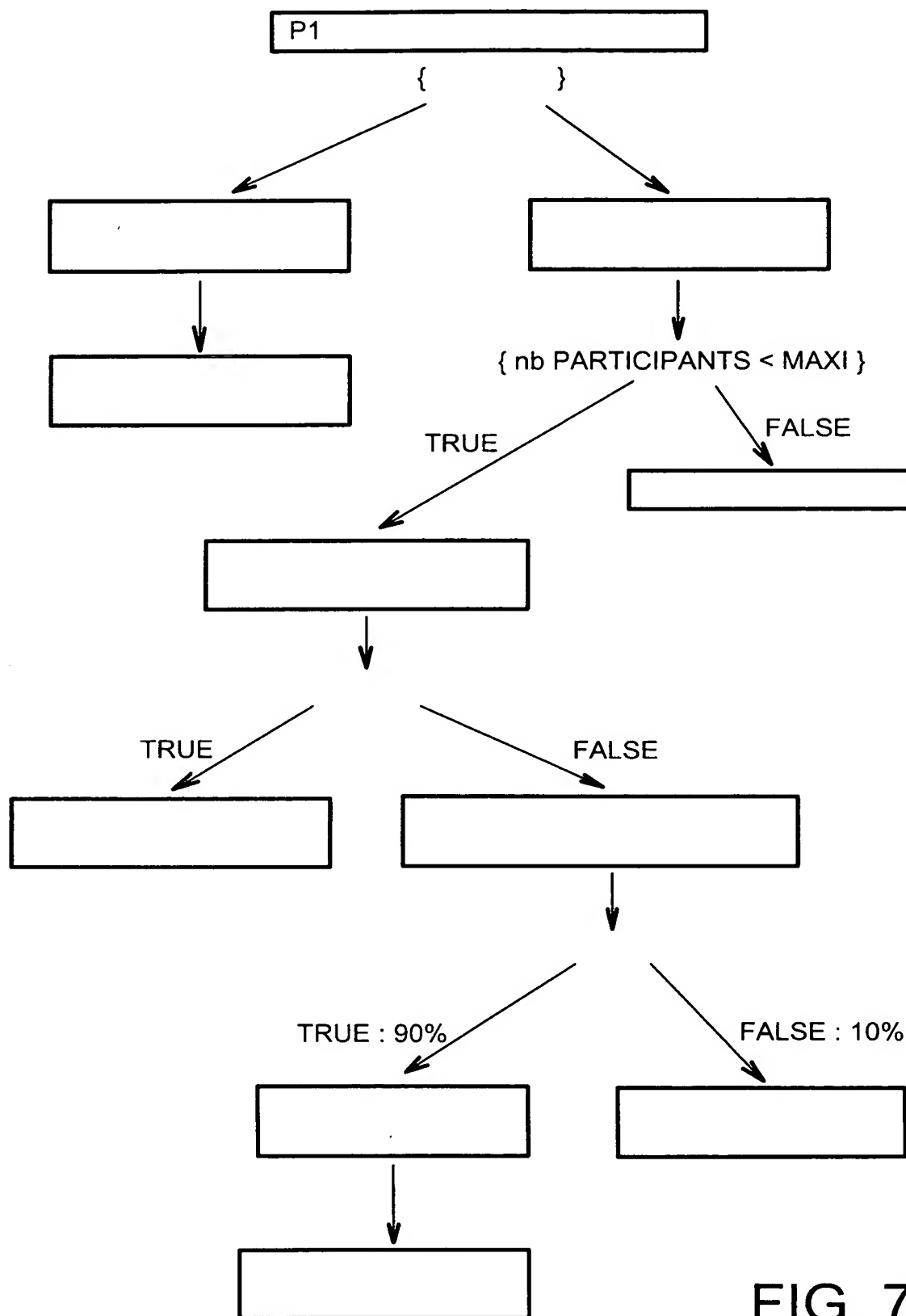


FIG. 7

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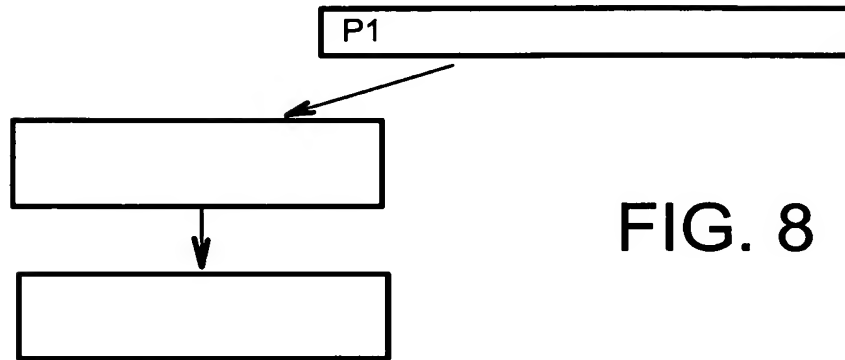


FIG. 8

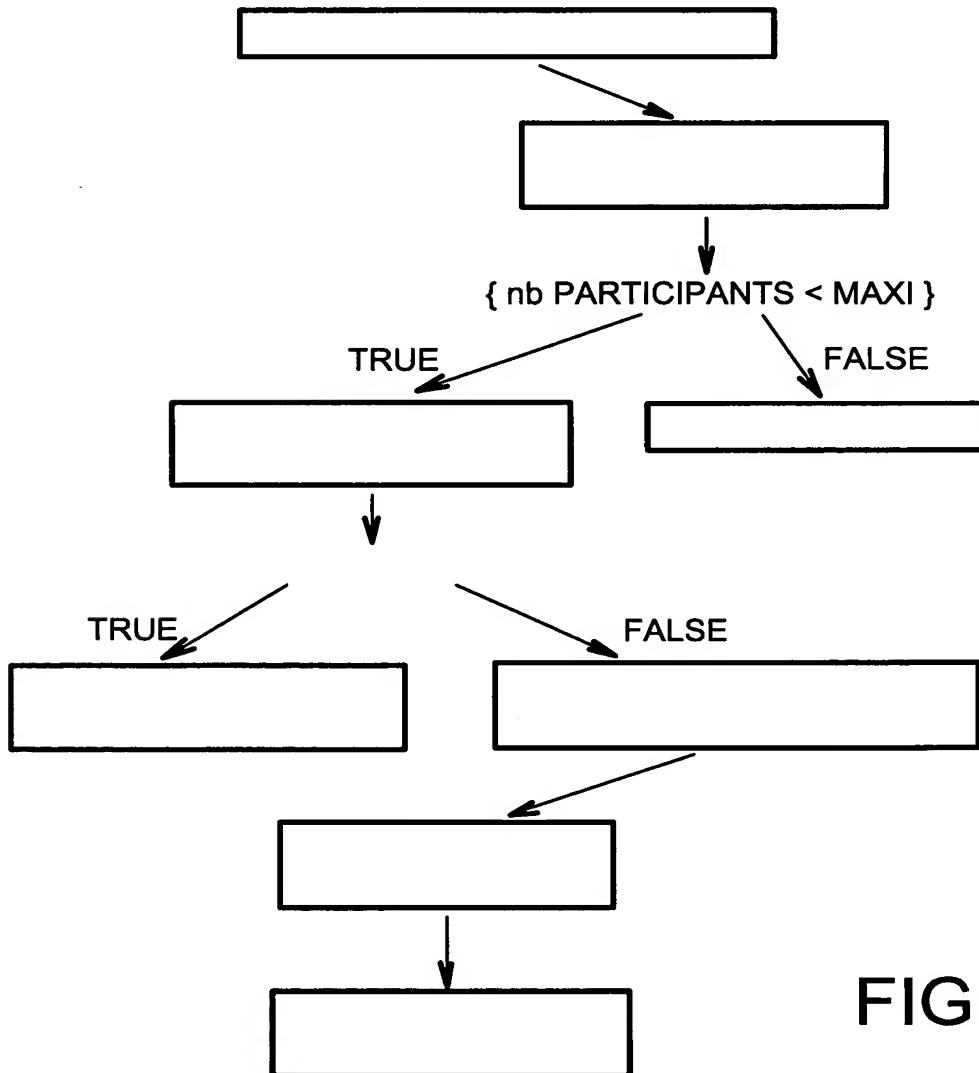


FIG. 9

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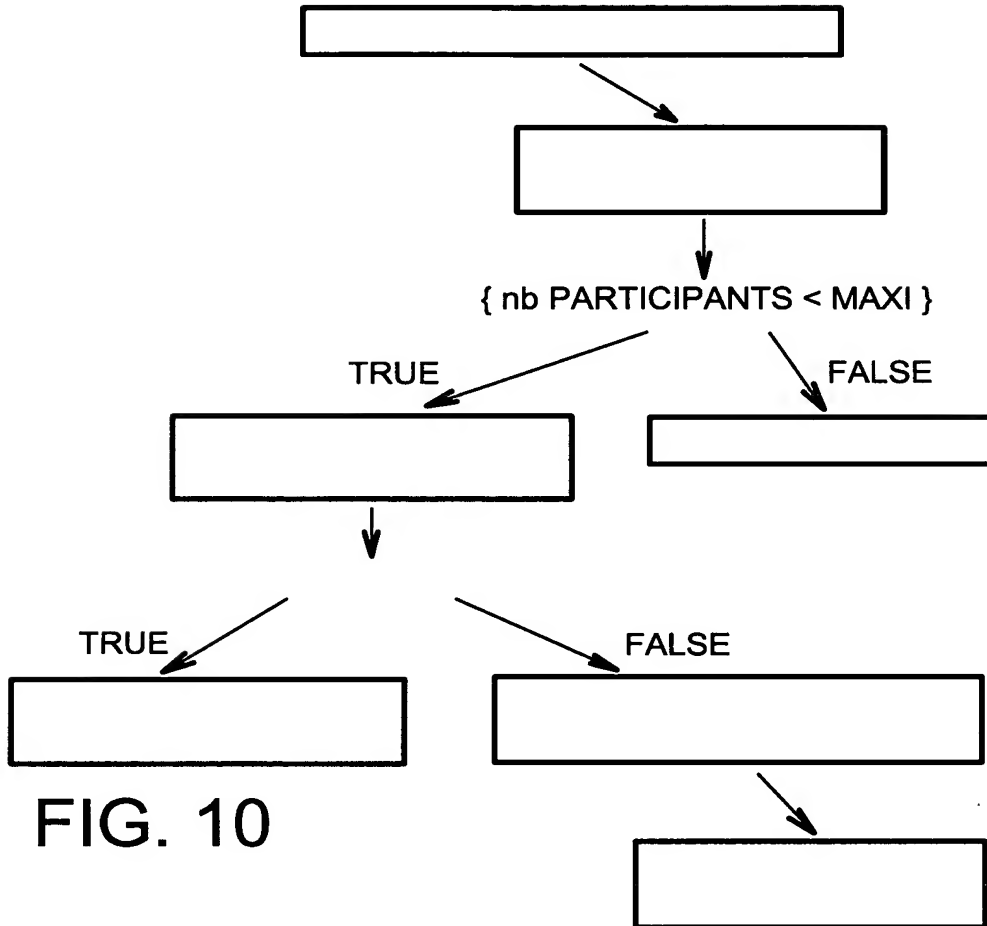
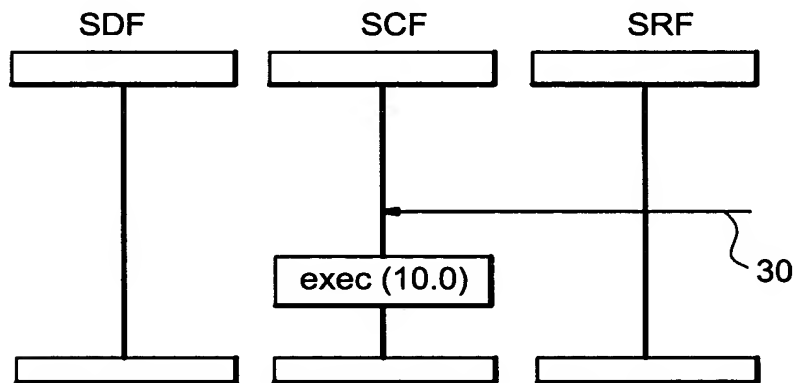


FIG. 10

FIG. 11





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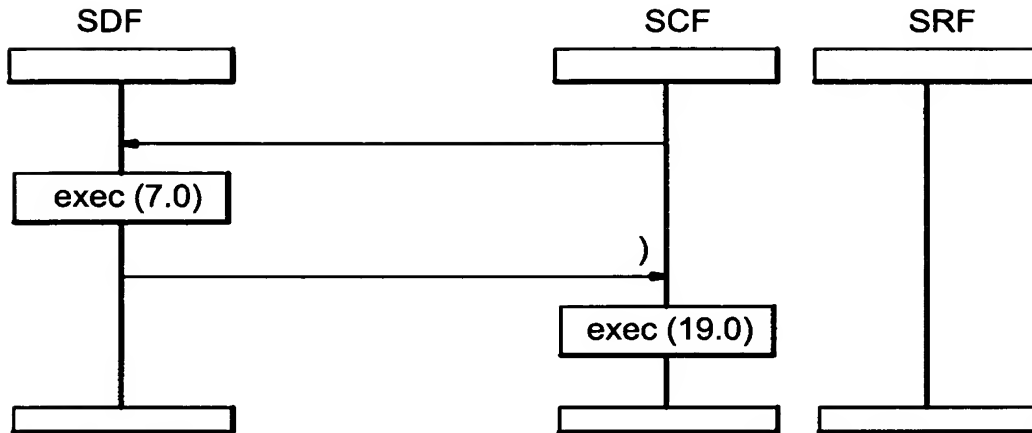


FIG. 12

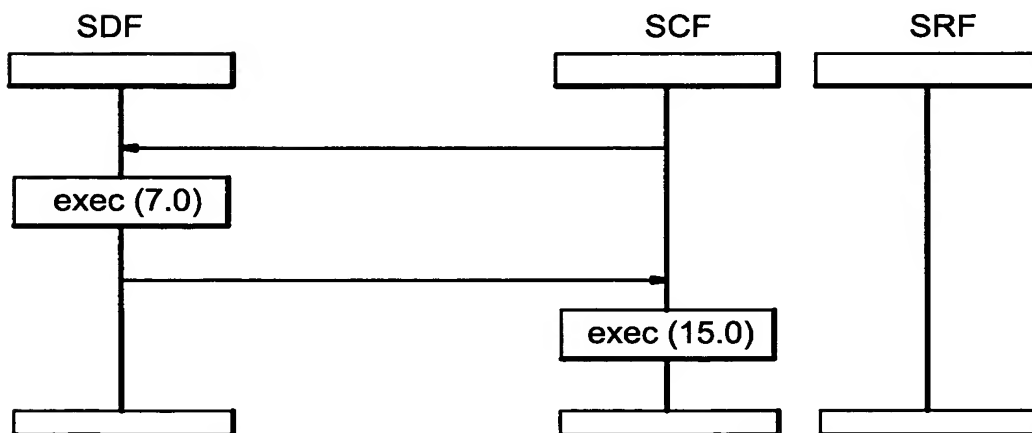


FIG. 13

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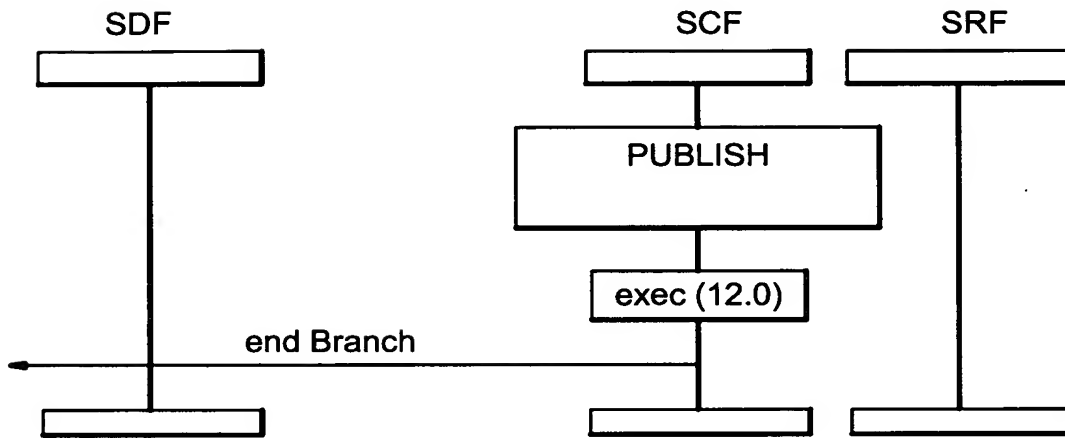


FIG. 14

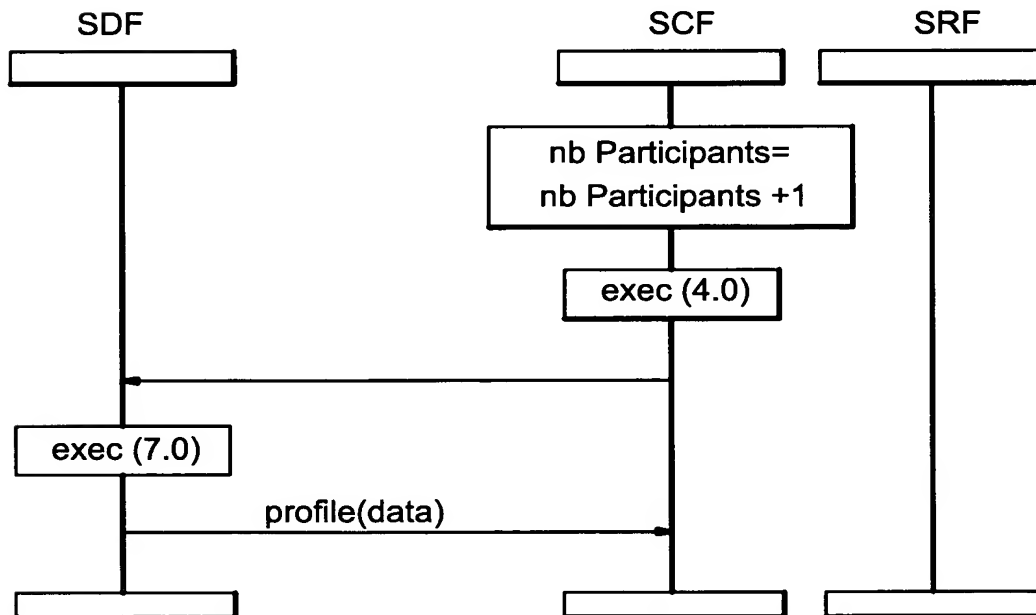


FIG. 15

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FIG. 16

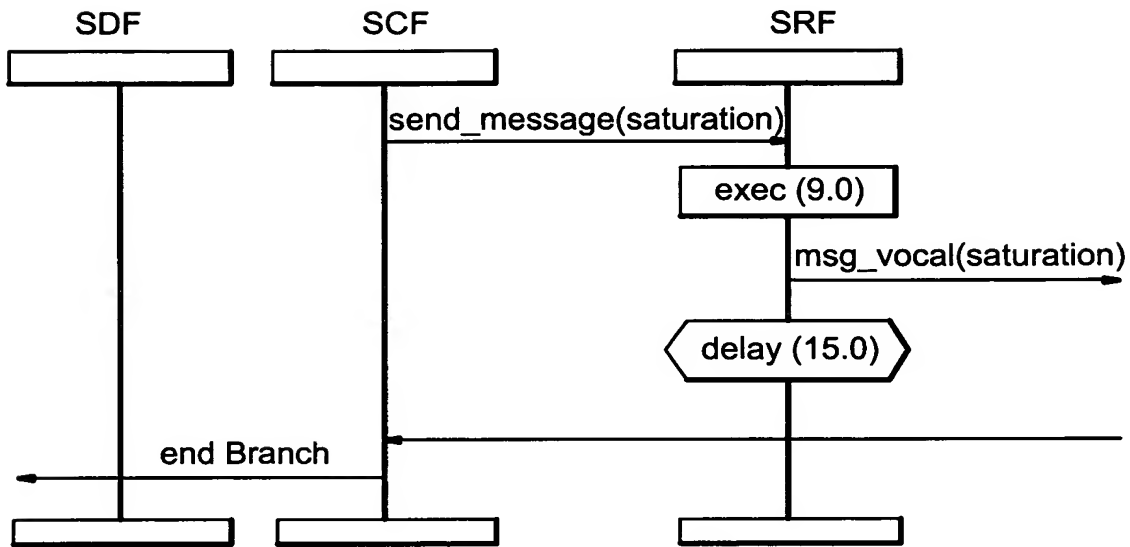
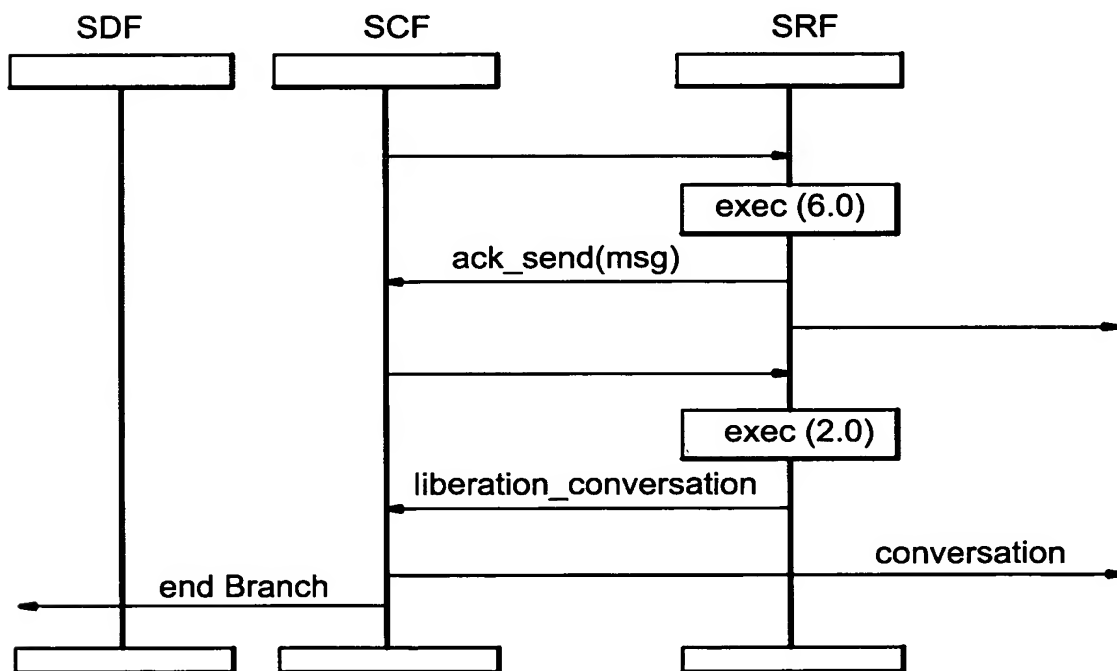


FIG. 17



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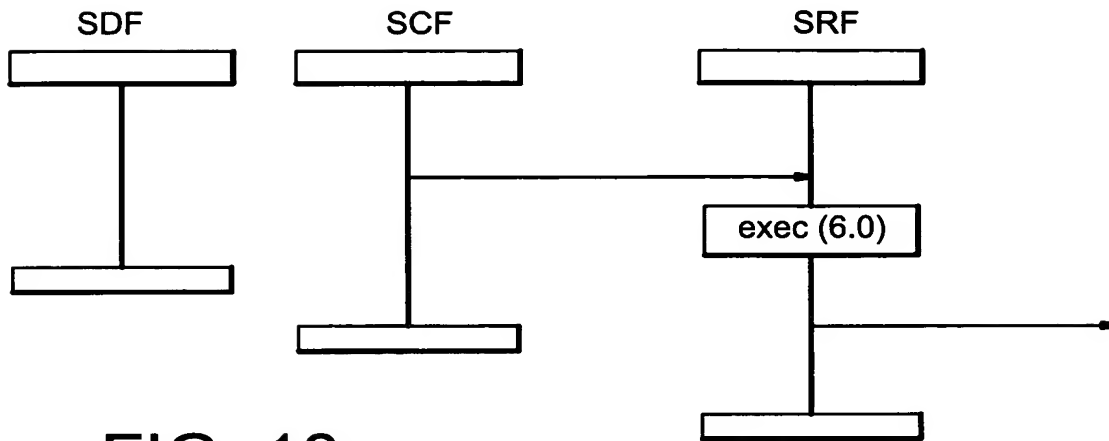


FIG. 18

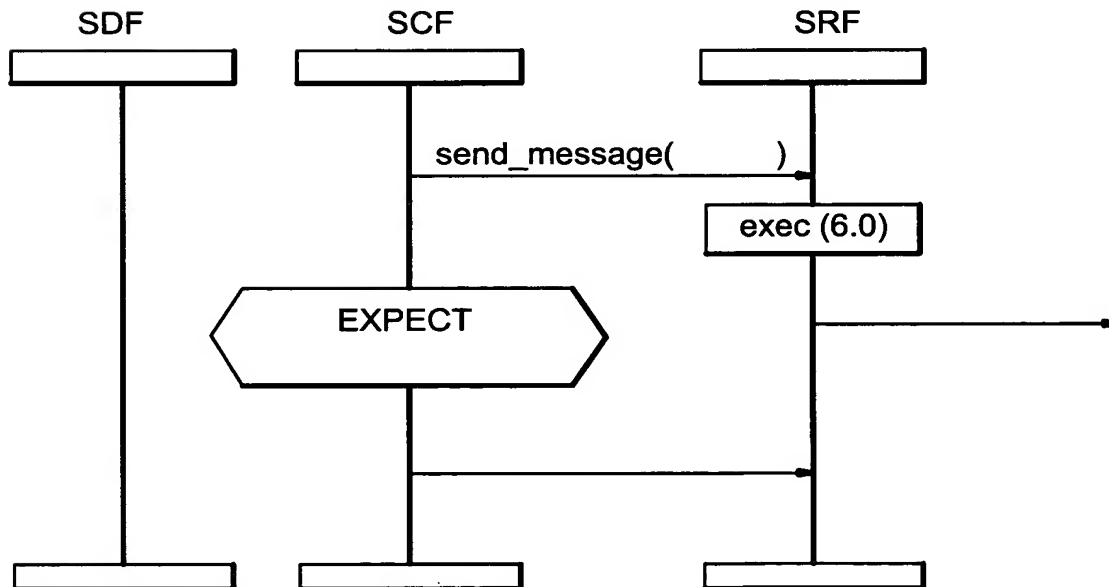


FIG. 19

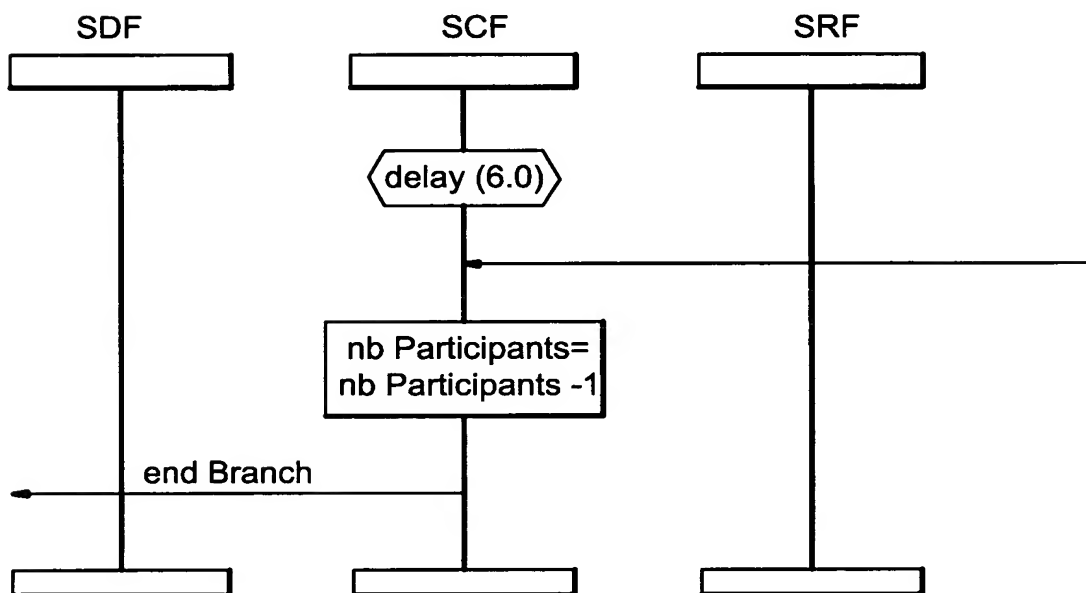


FIG. 20

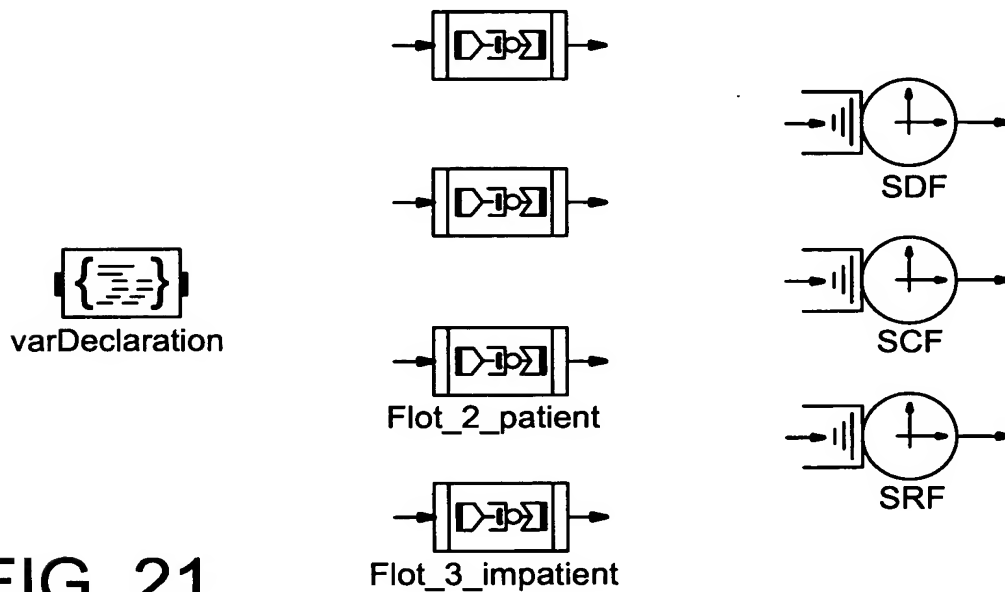


FIG. 21



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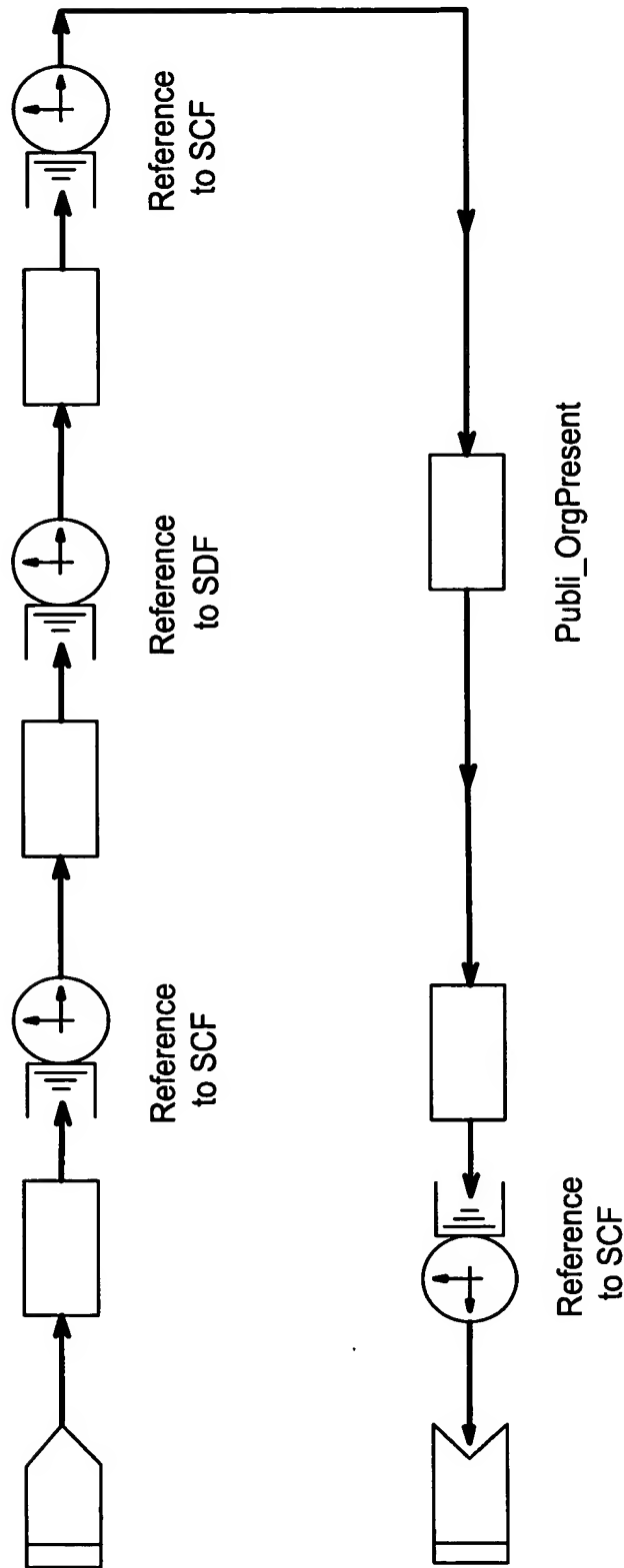


FIG. 23

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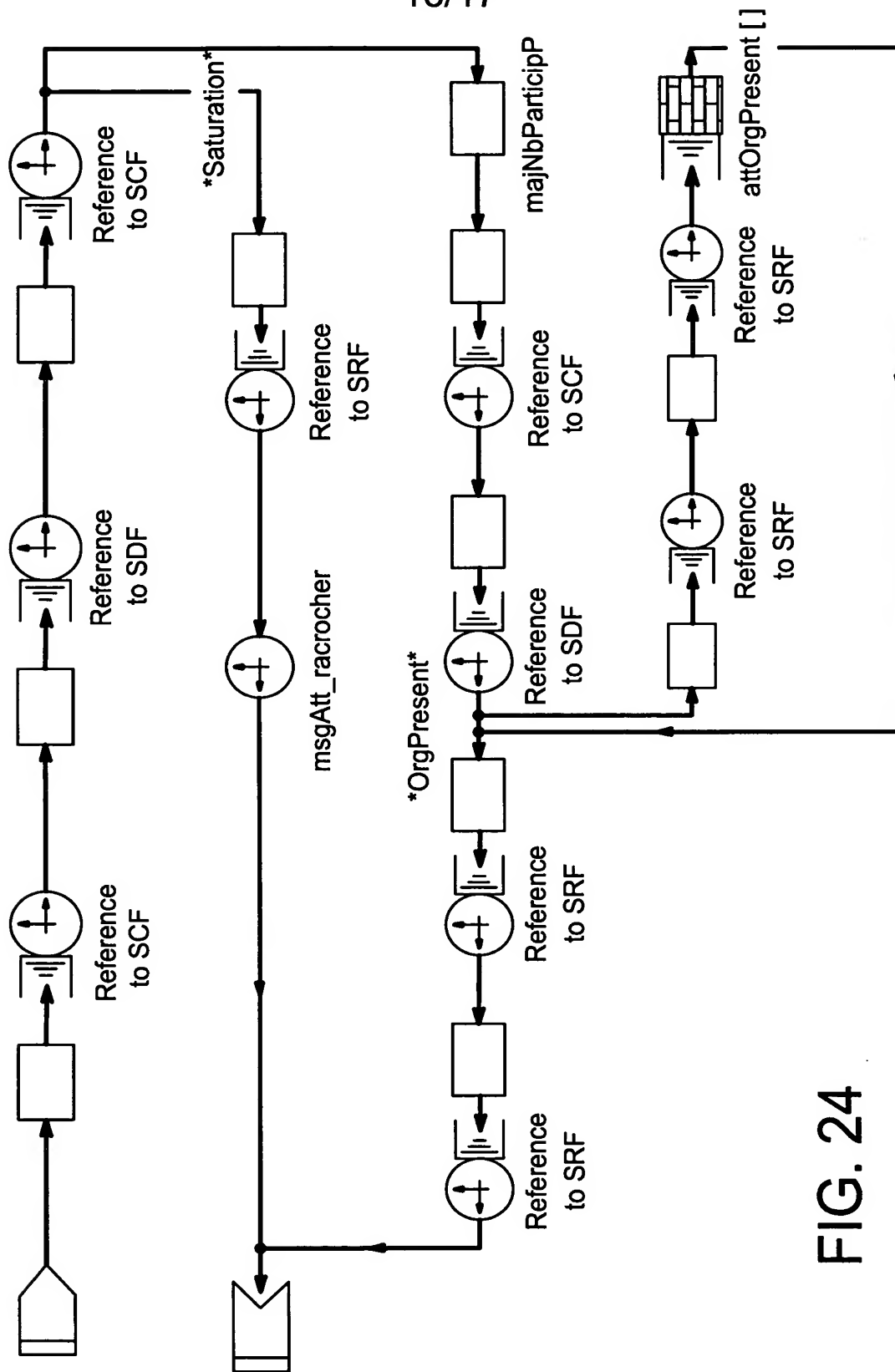


FIG. 24



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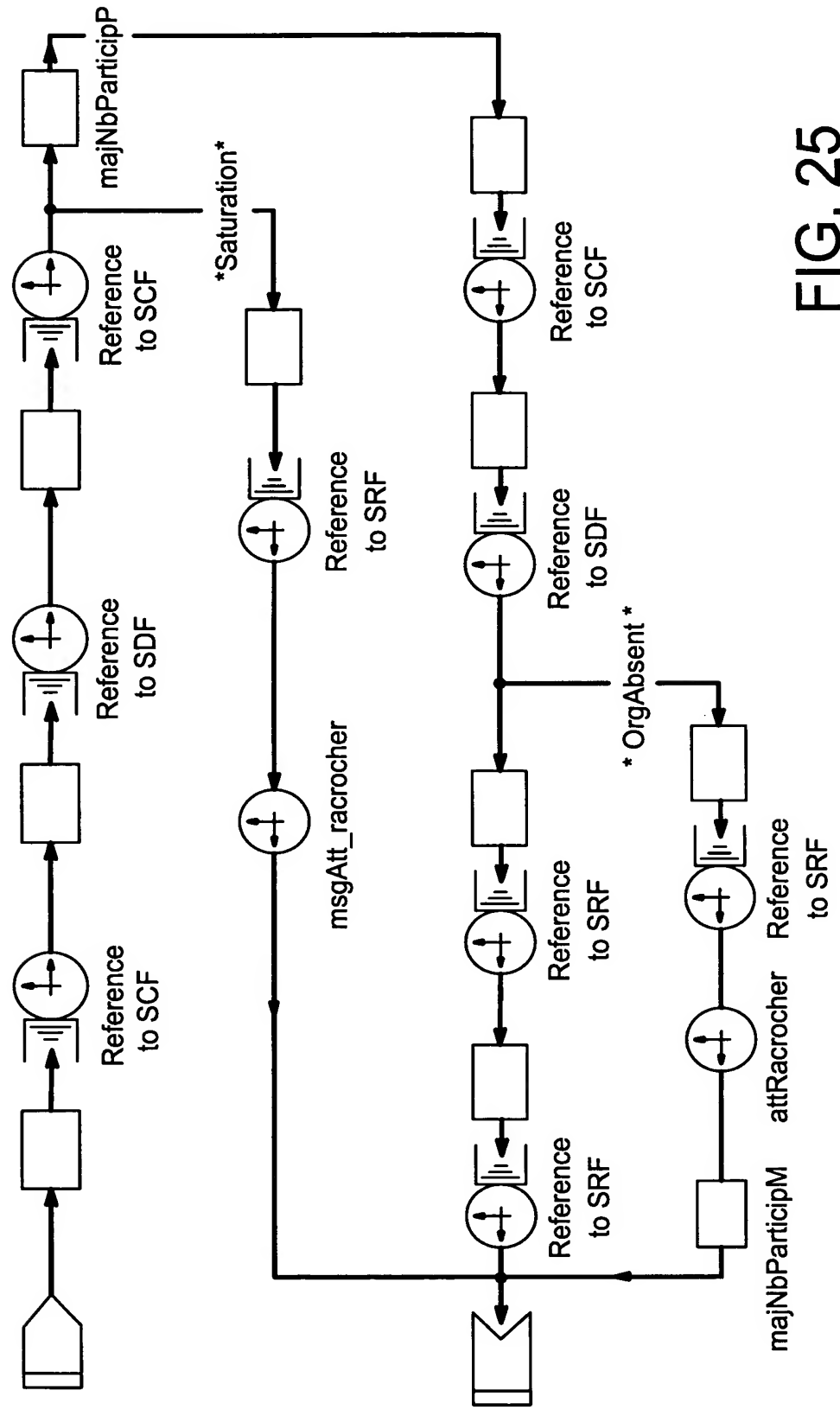


FIG. 25